

In the Claims:

Please amend claims 1-2 and 5-7, and add new claims 8-11 as indicated below.
This listing of claims replaces all prior versions.

1. (Currently amended) A method of manufacturing a semiconductor device on a region of silicon oxide situated next to a region of monocrystalline silicon at a surface of a semiconductor body, ~~a non-monocrystalline auxiliary layer is formed in two process steps~~, the method comprising the steps of:

1) forming a layer of arsenic on the region of monocrystalline silicon by heating the semiconductor body in an atmosphere with an arsenic compound, the layer of arsenic not being formed on the region of silicon oxide; and, thereafter,

2) forming a layer of non-monocrystalline silicon as an auxiliary layer on the region of silicon oxide by heating the semiconductor body in an atmosphere ~~comprising that includes~~ a gaseous silicon compound ~~instead of and that does not include~~ a gaseous arsenic compound, the layer of non-monocrystalline silicon not being forming on the region of monocrystalline silicon.

2. (Currently amended) A method as claimed in claim 1, wherein ~~during the forming of the auxiliary layer, the semiconductor body is heated during Step 1) in an atmosphere comprising, in addition to the gaseous arsenic compound, the gaseous silicon compound used during Step 2).~~

3. (Previously presented) A method a claimed in claim 1, wherein Step 2) is ended before deposition from the silicon compound takes place on the arsenic layer formed on the region of monocrystalline silicon.

4. (Previously presented)A method as claimed in claim 1, wherein during the forming of the auxiliary layer, the semiconductor body is heated step 1) and step 2) at a temperature in the range between 400 and 600°C in an atmosphere with a pressure below 500 mTorr.

5. (Currently amended) A method as claimed in claim 1, wherein further comprising, after the formation of the auxiliary layer, depositing a silicon-containing layer is deposited on the arsenic layer and on the auxiliary layer by heating the semiconductor body in an atmosphere comprising that includes a gaseous silicon compound, thereby forming a monocrystalline layer on the region of monocrystalline silicon and forming a polycrystalline layer on the auxiliary layer.
6. (Currently amended) A method as claimed in claim 5, wherein as the silicon-containing layer[,] is a layer of $\text{Si}_{1-x}\text{Ge}_x$ is deposited, where $0.05 < x < 0.20$, and to which less than 0.2 at. % carbon is added, and wherein by heating the semiconductor body is heated in an atmosphere comprising a silicon compound and a germanium compound.
7. (Currently amended) A method as claimed in claim 5, wherein further comprising forming the region of monoerystalline siliceon, an n-type collector zone of a bipolar transistor in the region of monocrystalline silicon is formed, and in the monocrystalline layer of $\text{Si}_{1-x}\text{Ge}_x$ deposited thereon, forming a p-type base zone of this the bipolar transistor is formed in the first monocrystalline layer.
8. (New) A method as claimed in claim 5, further comprising depositing a silicon layer on the arsenic layer and on the monocrystalline layer and on the polycrystalline layer by heating the semiconductor body in an atmosphere that includes a gaseous silicon compound, thereby forming a monocrystalline layer of silicon on the monocrystalline layer and forming a polycrystalline layer of silicon on the polycrystalline layer.
9. (New) A method as claimed in claim 8, further comprising depositing a silicon oxide layer on the monocrystalline layer of silicon and on the polycrystalline layer of silicon, etching the silicon oxide layer to form a window that exposes part of the monocrystalline layer of silicon, and providing an n-type doped polycrystalline silicon conductor track that contacts the monocrystalline layer of silicon through the window.

10. (New) A method as claimed in claim 5, further comprising removing the layer of arsenic prior to depositing the silicon-containing layer, wherein the silicon-containing layer is deposited on the region of monocrystalline silicon.
11. (New) A method as claimed in claim 1, wherein the semiconductor body further includes a buried layer and a contact zone that is situated next to the region of silicon oxide at the surface of the semiconductor body, the contact zone extending from the surface of the semiconductor body to the buried layer, and wherein the layer of arsenic is also formed on the contact zone during step 1) and the layer of non-monocrystalline silicon is not formed on the contact zone during step 2).